

What is claimed is:

1. A multi-domain liquid crystal display device, comprising:

5 first and second substrates;

a liquid crystal layer between the first and second substrates;

data lines for applying a data signal on the first substrate;

10 gate lines crossing the data lines to apply a gate signal;

pixel electrodes for driving a liquid crystal of the liquid crystal layer;

15 switching devices arranged at each intersection between the gate lines and the data lines; and

auxiliary electrode lines extended vertically from the gate lines to control an orientation of the liquid crystal with the gate lines.

20 2. The device according to claim 1, wherein the auxiliary electrode lines are formed between the pixel electrode and the data line at the outside of the pixel electrode.

25 3. The device according to claim 1, further comprising a common electrode on the second substrate.

4. The device according to claim 3, the common electrode includes an opening area.

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5. The device according to claim 1, further comprising an dielectric structure on the second substrate.

6. A multi-domain liquid crystal display device,  
comprising:

data lines for applying a data signal on a first  
substrate ;

5 gate lines crossing the data lines to apply a gate  
signal;

common electrode on a second substrate;

a liquid crystal layer between the first and second  
substrates;

10 pixel electrodes for driving a liquid crystal of the  
liquid crystal layer;

switching devices arranged at each intersection  
between the gate lines and the data lines; and

auxiliary electrode lines extended vertically from  
15 the gate lines to control an orientation of the liquid  
crystal with the gate lines.

7. The device according to claim 6, the liquid crystal  
layer includes a positive anisotropy.

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8. The device according to claim 6, the liquid crystal  
layer includes a negative anisotropy.

9. The device according to claim 6, the liquid crystal  
25 layer includes chiral dopants.

10. The device according to claim 6, further comprising a  
phase-differential film on at least one of the first and  
second substrates.

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11. The device according to claim 10, the phase-  
differential film includes a negative uniaxial film.

12. The device according to claim 10, the phase-differential film includes a negative biaxial film.

13. The device according to claim 6, further comprising a  
5 dielectric structure on the second substrate.

14. The device according to claim 6, further comprising an opening area in the common electrode.

10 15. A method for fabricating a multi-domain liquid crystal display device comprising the steps of:

forming a liquid crystal layer between a first and second substrates;

forming data lines for applying a data signal on the  
15 first substrate;

forming gate lines crossing the data lines to apply a gate signal;

forming pixel electrodes for driving a liquid crystal of the liquid crystal layer;

20 forming switching devices arranged at each intersection between the gate lines and the data lines; and

forming auxiliary electrode lines extended vertically from the gate lines to control an orientation of the  
25 liquid crystal with the gate lines.

16. The method according to claim 15, further comprising the steps of forming a dielectric structure on the second substrate.

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17. The method according to claim 15, further comprising the steps of forming a common electrode on the second substrate.

18. The method according to claim 17, further comprising the steps of forming an opening area in the common electrode.

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19. A method for fabricating a multi-domain liquid crystal display device comprising the steps of:

forming data lines for applying a data signal on a first substrate ;

10 forming gate lines crossing the data lines to apply a gate signal on the first substrate;

forming common electrode on a second substrate;

forming a liquid crystal layer between the first and second substrates;

15 forming pixel electrodes for driving a liquid crystal of the liquid crystal layer;

forming switching devices at each intersection between the gate lines and the data lines; and

20 forming auxiliary electrode lines extended vertically from the gate lines to control an orientation of the liquid crystal with the gate lines.

20. The method according to claim 16, further comprising the steps of forming a dielectric structure on the  
25 second substrate.

21. The method according to claim 16, further comprising the steps of forming an opening area in the common electrode.

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